IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS: Chatterjee et al.

SERIAL NO.:   10/788,943

FILING DATE: February 26, 2004

TITLE: Memory Unit with Controller Managing Memory Access through JTAG and CPU Interfaces

EXAMINER: Kerveros, James C

ART UNIT: 2138

ATTY. DKT. NO.:   IDT-1872

MAIL STOP AMENDMENT
COMMISSIONER FOR PATENTS
P.O. BOX 1450
ALEXANDRIA, VA 22313-1450

AMENDMENT B

Dear Sir:

    In response to the Notice Requiring Excess Claims Fees mailed on 5/3/2007, please enter the following amendments in the above-identified Application and consider the following remarks.

Amendments to the Claims begin on page 2 of this paper.

Remarks begin on page 10 of this paper.